

FIG · 1

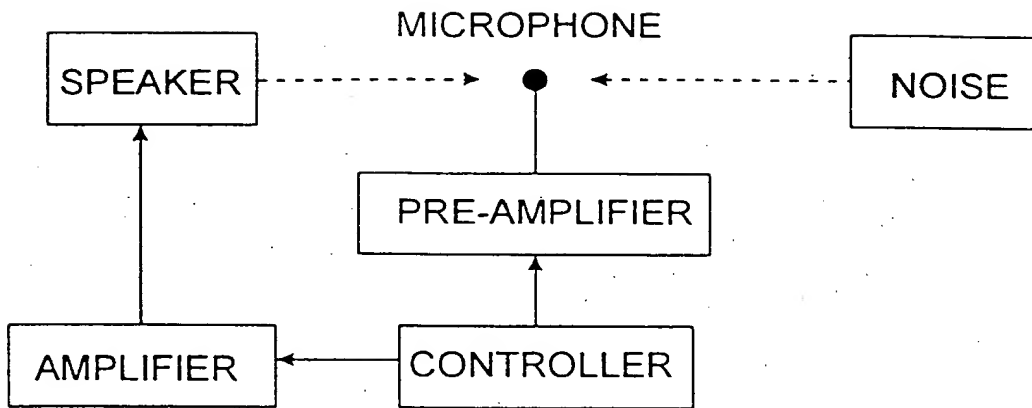


FIG · 2

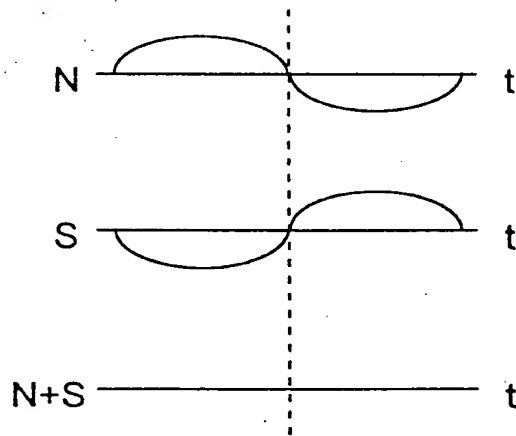


FIG. 3  
PRIOR ART

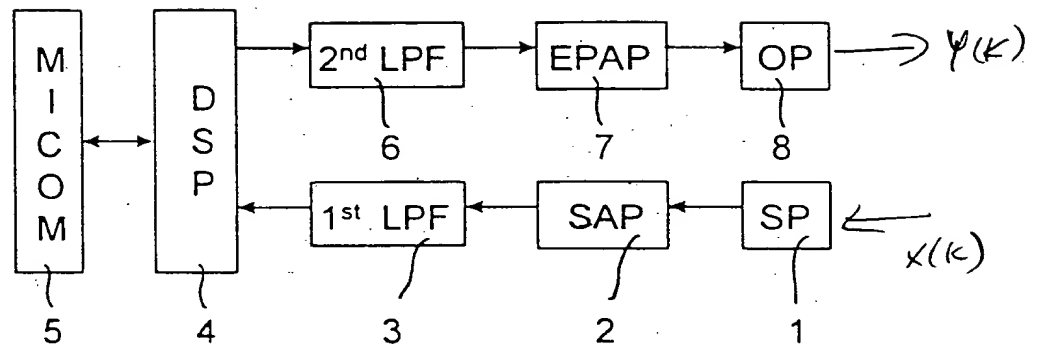


FIG. 4  
PRIOR ART

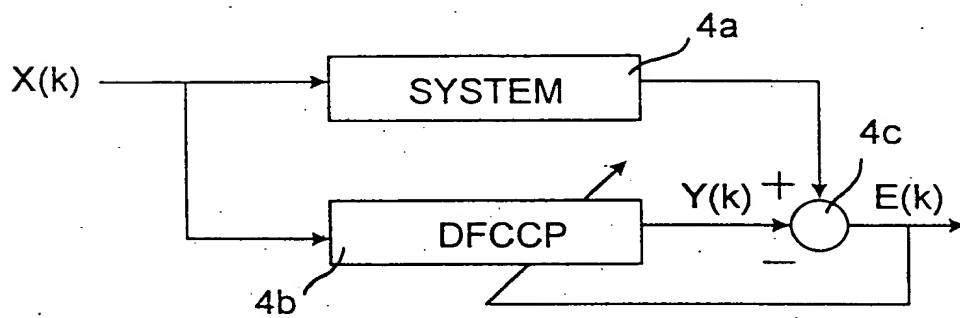


FIG · 5  
PRIOR ART

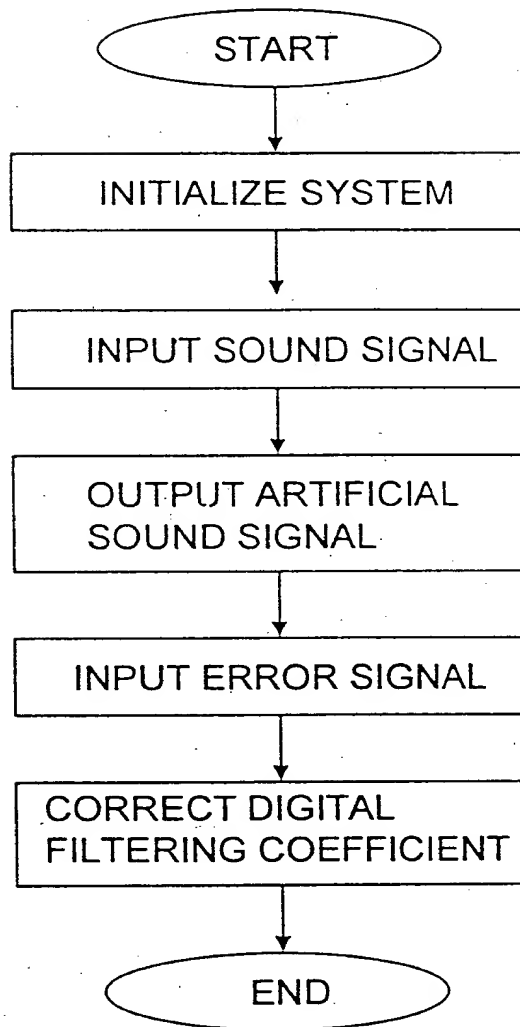


FIG. 6

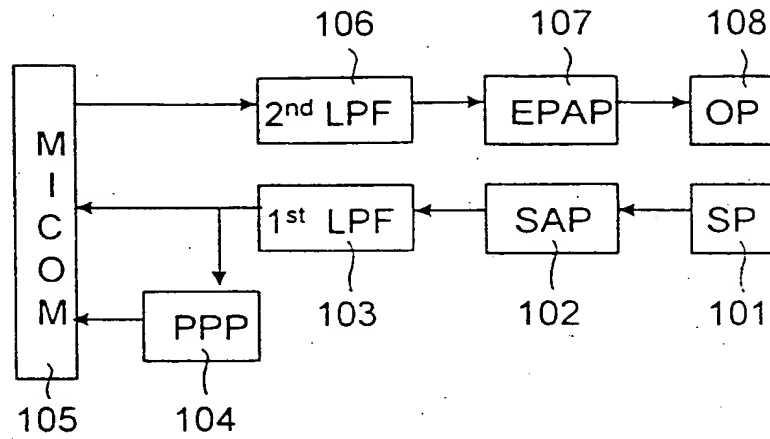


FIG. 7

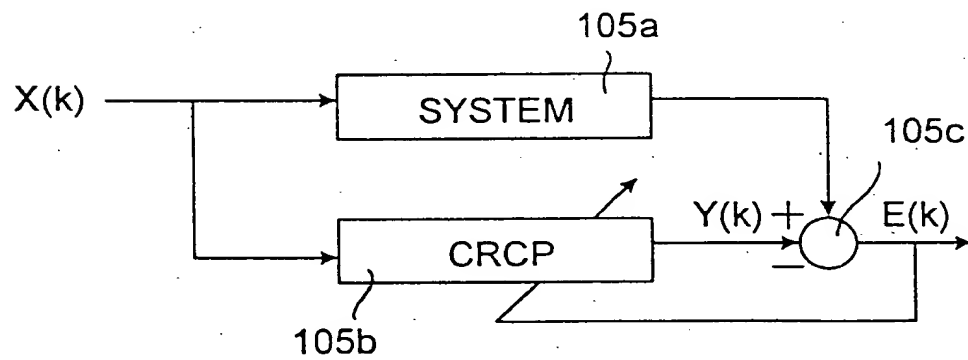


FIG - 8

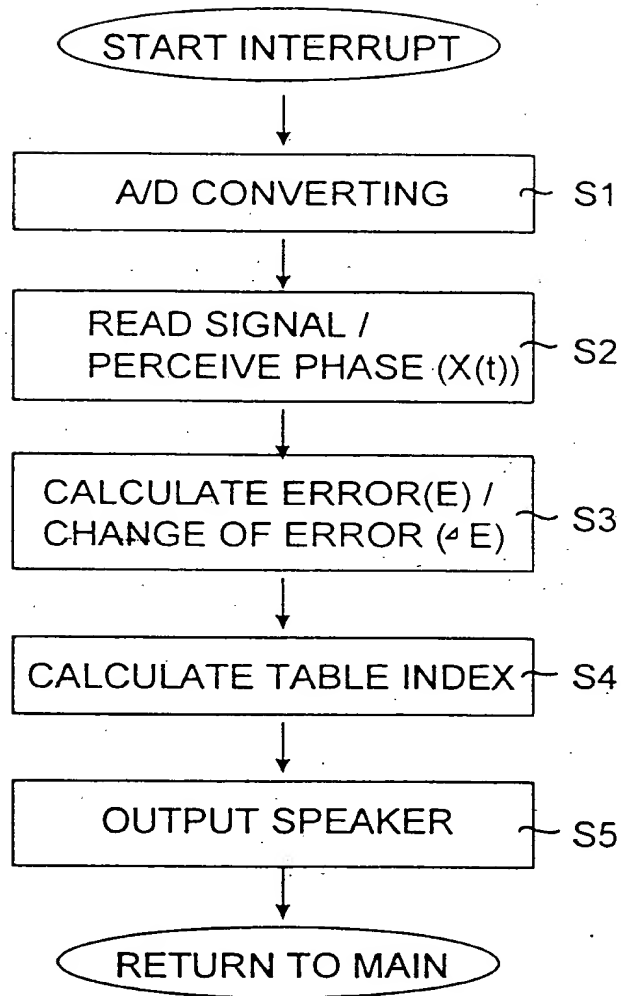
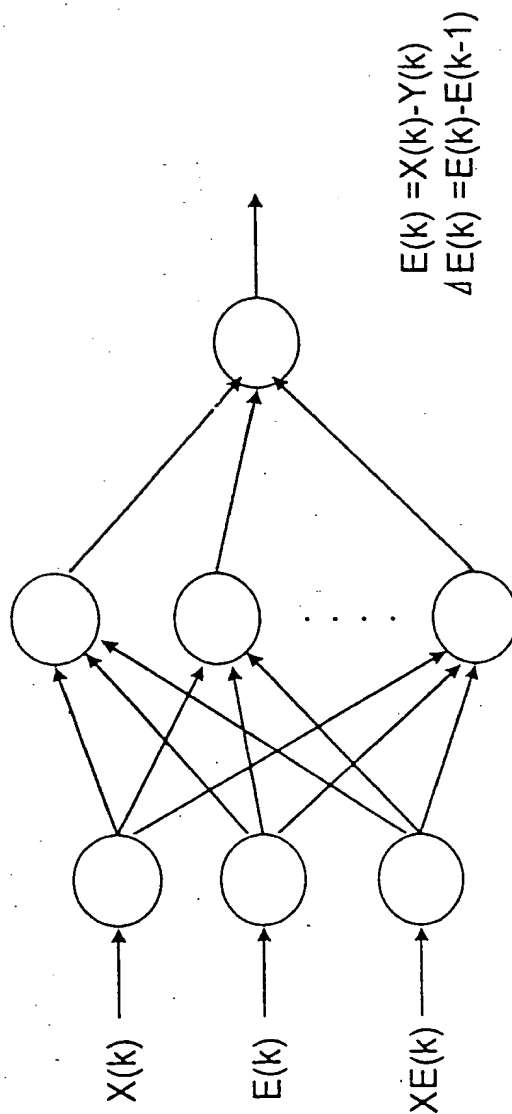
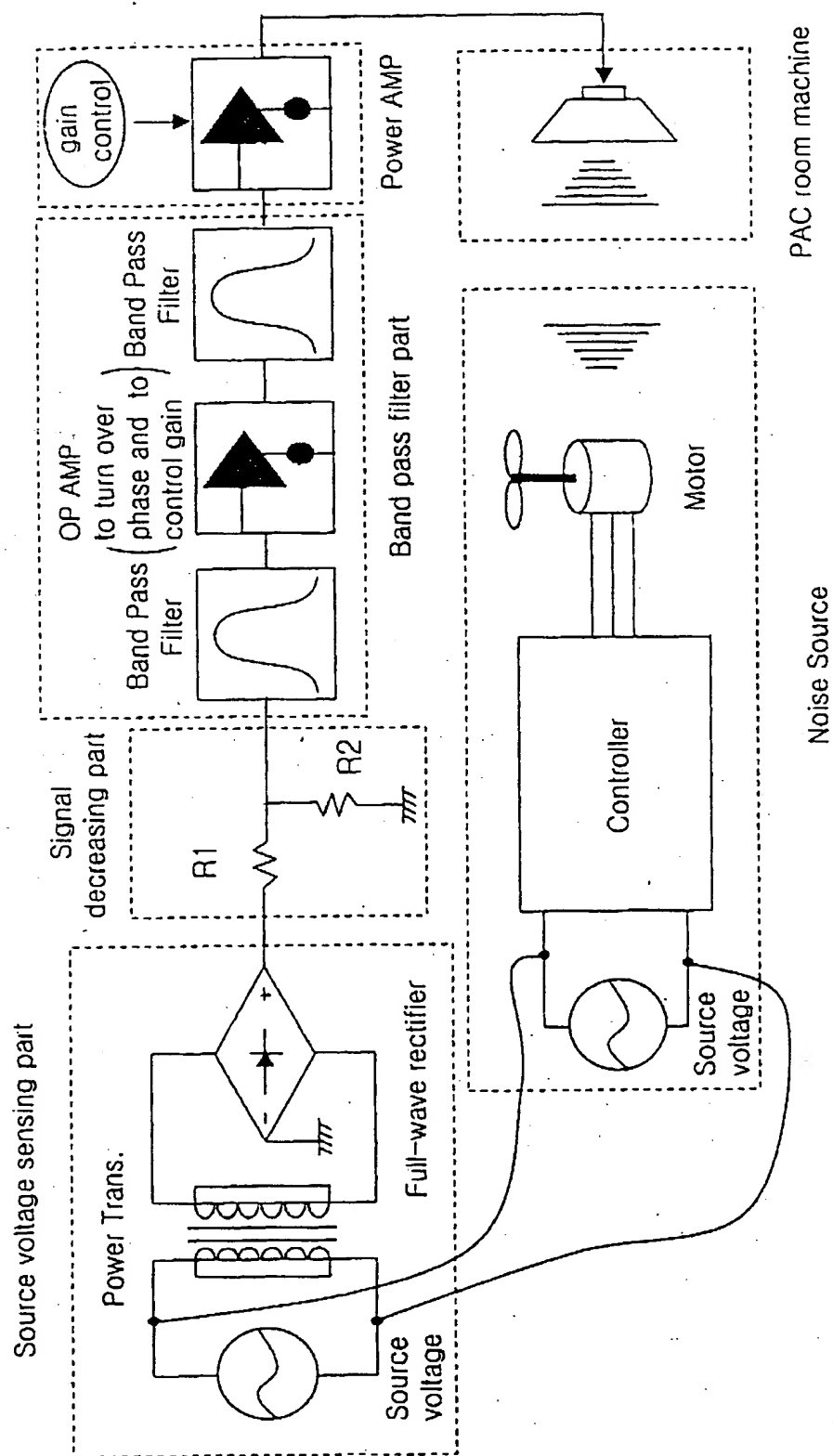


FIG · 9

INPUT STEP MIDDLE STEP OUTPUT STEP





**Fig. 10**